

April 19, 1996

CRITICAL ITEMS LIST

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1) CIL ITEM : F200-C0-01  
 2) FMEA CODE : F200-C0  
 3) COMPONENT : RVDY/LVDT EXCITATION VOLTAGE SOURCE  
 4) PART NUMBER : RE1493  
 5) SYSTEM/SUBSYSTEM : CONTROLLER/F200-XX  
 6) FAILURE MODE : FAILURE OF EXCITATION VOLTAGE IN THE CONTROLLER CHANNEL

7) PREPARED : SSME RELIABILITY  
 8) APPROVED :  
 9) DATE : 04-19-96  
 10) REVISION/CHANGE : -001/0  
 11) EFFECTIVITY : -05  
 12) HAZARD REFERENCE : SEE LISTINGS BELOW  
 13) CCID # : M63-01-3285

PHASE	FAILURE DESCRIPTION/EFFECT	CRITICALITY
N 4-2	<p>FAILURE OF EXCITATION VOLTAGE IN BOTH CHANNELS RESULTS IN THE DISQUALIFICATION OF BOTH OF CHANNELS. CONTROLLER INITIATED PNEUMATIC SHUTDOWN. MISSION ABORT. (SEE OPERATIONAL USE.)</p> <p>REDUNDANCY SCREENS: CONTROLLER SYSTEM: LIKE REDUNDANCY</p> <p>-----</p> <p>A: PASS. REDUNDANT HARDWARE ITEMS ARE CAPABLE OF CHECKOUT DURING NORMAL GROUND TURNAROUND.            B: PASS. LOSS OF A REDUNDANT HARDWARE ITEM IS DETECTABLE DURING FLIGHT.            C: PASS. LOSS OF REDUNDANT HARDWARE ITEMS COULD NOT RESULT FROM A SINGLE CREDIBLE EVENT.</p>	<p>1R HAZARD REF: ME-64H</p>

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CIL ITEM: F200-CB-01	DESIGN	DOCUMENT REF.
ALL CAUSES: OUTPUT ELECTRONICS: 2 KHz EXCITATION VOLTAGE FAILURE (1)		(1) HONEYWELL BLOCK 11 FMEA VOL V OE-F24
THE PROPELLANT VALVE RVDT POSITION INDICATORS ARE REDUNDANT ELEMENT PAIRS (1). A FAILURE IN BOTH CONTROLLER CHANNELS (MULTIPLE FAILURE), RESULTING IN LOSS OF FUNCTION, RESULTS IN HYDRAULIC LOCKUP (2). THE CONTROLLER (WITH SOFTWARE) IS CONFIGURED TO DETECT AND RESPOND PROPERLY TO OUT OF QUALIFICATION LIMIT FAILURES, IMPLEMENT THE APPROPRIATE REDUNDANCY MANAGEMENT RESPONSE, AND COMMAND A SAFE ENGINE STATE WHEN CONTROLLER REDUNDANCY IS LOST (2). HOWEVER, THE CONTROLLER (WITH SOFTWARE) IS NOT CAPABLE OF DETECTING OR RESPONDING TO A FAILURE WHICH RESULTS IN THE LIMITS BEING MONITORED FAILING WITHIN ENGINE REDLINE LIMITS OR WITHIN NORMAL ENGINE OPERATING CONDITIONS (2).		(1) DSHG8977A1 (2) CP4068000B 3.1.3:4 3.2.1:6.3 3.2.1:6.4 3.2.3:2.3 3.2.3:3 3.2.3:6 3.2.4
ELECTRICAL, MECHANICAL, AND ELECTROMECHANICAL PARTS FOR THE CIRCUITS INVOLVED IN THIS FUNCTION HAVE BEEN SELECTED FROM THE CLASS S OR EQUIVALENT APPROVED PARTS SELECTION LIST (1), QUALITY ASSURANCE AND SCREENING REQUIREMENTS FOR HIGH RELIABILITY MICROCIRCUITS (2), AND THE SPECIFICATION CONTROL DRAWING FOR MICROCIRCUITS, MONOLITHIC SILICON, AND TRANSISTOR-TRANSISTOR LOGIC (TTL) FAMILY DEVICES (3). PRIOR TO INSTALLATION ON THE CARD ASSEMBLIES, COMPONENTS ARE SUBJECTED TO A BURN-IN PERIOD WHERE PARTS ARE EXERCISED AT TEMPERATURES IN EXCESS OF NORMAL CONTROLLER OPERATING ENVIRONMENT, BUT LESS THAN COMPONENT MAXIMUM SPECIFIED OPERATING ENVIRONMENT, TO SCREEN FOR INFANT MORTALITY (4). IN ADDITION TO THESE REQUIREMENTS, ALL COMPONENTS ARE DERATED FROM THE MAXIMUM RATING AT OPERATING EXTREMES (5). CLEANLINESS AND ALL PROCESSES USED DURING MANUFACTURE ARE CONTROLLED BY SPECIFICATION REQUIREMENTS (5).		(1) B5M03928, B5M03929 (2) B5M02704 (3) B5M03766 (4) B5M03876 (5) DSHG8977A1
THE CONTROLLER DESIGN MEETS ALL CEJ FAIL-OPERATE/FAILSAFE REQUIREMENTS FOR THIS FAILURE MODE (1). REDUNDANT CONTROLLER CHANNEL FUNCTIONS ARE PHYSICALLY SEPARATED WITHIN THE CONTROLLER HOUSING (2). CIRCUITS ARE DESIGNED TO PREVENT BRIDGING (2), ALL EEE PARTS ARE REQUIRED TO HAVE CONFORMAL COATING, AND INTERNAL WIRING IS INSULATED TO PREVENT SHORT CIRCUITS FROM CONDUCTIVE CONTAMINATION (3). ALL ELECTRICAL COMPONENTS ARE CONTAINED WITHIN THE CASE STRUCTURE WHICH IS PRESSURIZED WITH A POSITIVE PRESSURE INERT GAS BACKFILL TO PREVENT CONTAMINATION (2). WHERE APPLICABLE, ELECTROMAGNETIC INTERFERENCE SHIELDING IS PROVIDED AND CIRCUIT INTERCONNECTS USE TWISTED PAIR WIRING (2). ALL CIRCUIT CARDS ARE KEYED TO THEIR RESPECTIVE CONNECTION LOCATIONS TO PRECLUDE IMPROPER INSTALLATION (2). RAMP CLAMPS ARE UTILIZED TO PRECLUDE VIBRATION INDUCED CARD FAILURES (2). DESIGN OF CIRCUIT CARDS AND DETERMINATION OF COPPER PATH TRACE SPACING, WEIGHT, AND WIDTH IS CONTROLLED BY SPECIFICATION (2). EACH UNIT (PRODUCTION AND RECYCLE) IS REQUIRED TO PASS A FUNCTIONAL ACCEPTANCE TEST UNDER ENVIRONMENTAL CONDITIONS BEYOND THOSE SEEN DURING NORMAL FIELD OPERATION WITHOUT DEGRADATION OF HARDWARE LIFE EXPECTANCY (2). A WORST CASE CIRCUIT ANALYSIS WAS PERFORMED TO VERIFY NOMINAL OPERATION AT SPECIFICATION LIMITS (4). AN ANALYSIS WAS PERFORMED BY HONEYWELL TO ASSURE NO SINGLE POINT COMPONENT FAILURES ARE INHERENT TO THE CONTROLLER DESIGN (5).		(1) CP320N0003B (2) DSHG8977A1 (3) B5M03928 (4) ES24472-01 (5) HONEYWELL BLOCK 11 FMEA VOL 1

CIL ITEM: F200-CB-01		DESIGN	DOCUMENT REF.
DESIGN QUALIFICATION TESTING OF THE CONTROLLER ASSEMBLY HAS BEEN PERFORMED INCLUDING: FAULT INSERTION TESTING (1), ASSEMBLY THERMAL CYCLING (2), CASE AND ASSEMBLY VIBRATION TESTING (3), ELECTROMAGNETIC INTERFERENCE AND SUSCEPTIBILITY TESTING (4), ACOUSTICAL NOISE TESTING (5), CASE ULTIMATE PRESSURE TESTING (6), AND ASSEMBLY BREAK OPEN INSPECTION (7).			(1) TR340792B2 TR34080202 TR34088258 TR34088259 (2) TR34080203 TR34080207 (3) TR34080204 TR34080205 TR34080206 TR34087499 (4) TR34080209 (5) TR34080210 (6) TR34085021 (7) TR34085022
CIL ITEM: F200-CB-01		INSPECTION AND TEST	DOCUMENT REF.
POSSIBLE CAUSES	SIGNIFICANT CHARACTERISTICS	INSPECTION(S)/TEST(S)	DOCUMENT REF.
ALL CAUSES:	RE1493 - CONTROLLER		RE1493
	ASSEMBLY INTEGRITY	CLEANLINESS REQUIREMENTS ARE VERIFIED PER SPECIFICATION DURING MANUFACTURING OF THE CONTROLLER ASSEMBLY.  ENVIRONMENT CONTROLS (TEMPERATURE, HUMIDITY) ARE ENFORCED DURING ASSEMBLY AND TESTING PER SPECIFICATION REQUIREMENTS.  TO PREVENT COMPONENT DAMAGE, STATIC ELECTRICAL DISCHARGE POTENTIAL IS CONTROLLED DURING MANUFACTURING PER SPECIFICATION REQUIREMENTS.	RC1493  DSNG8977A1  DSNG8977A1
	MATERIAL INTEGRITY	MATERIAL INTEGRITY IS VERIFIED PER SPECIFICATION REQUIREMENTS.	RC1493

CIL ITEM: F200-CB-01		INSPECTION AND TEST	
POSSIBLE CAUSES	SIGNIFICANT CHARACTERISTICS	INSPECTION(S)/TEST(S)	DOCUMENT REF.
	INTEGRITY OF ELECTRONICS	<p>PROTECTIVE FINISHES AND MATERIAL SELECTION TO PREVENT DETRIMENTAL EFFECTS FROM ENVIRONMENTAL EXPOSURE, STRESS CORROSION, AND ELECTROLYTIC CORROSION ARE VERIFIED PER SPECIFICATION REQUIREMENTS.</p> <p>FLAMMABILITY REQUIREMENTS ARE VERIFIED PER SPECIFICATION REQUIREMENTS.</p> <p>THE FOLLOWING PROCESSES ARE VERIFIED PER SPECIFICATION DURING MANUFACTURING OF THE CARD ASSEMBLIES:</p> <ul style="list-style-type: none"> <li>- CONSTRUCTION OF PRINTED CIRCUIT BOARDS.</li> <li>- INSTALLATION OF TERMINALS.</li> <li>- PLASTICS AND ELASTOMERS FOR ELECTRONIC ENCAPSULATION.</li> <li>- SOLDERED ELECTRICAL CONNECTIONS.</li> <li>- POST-SOLDERING INSPECTION FREE OF SPLATTER AND CONTAMINATION.</li> <li>- ELECTRICAL BONDING.</li> <li>- COMPONENT LEAD AND INTERCONNECTION MATERIAL SELECTION.</li> <li>- FREE OF CONTAMINATION AFTER CONFORMAL COATINGS.</li> </ul>	<p>RC1493 MSFC-SPEC-250</p> <p>RC1493</p> <p>RL10005 RL10007 RL10008 RL10009 RL10009 RC1493 85M0392B RC1493</p>
	WELD INTEGRITY	<p>ALL WELDS ARE VERIFIED TO DRAWING AND SPECIFICATION REQUIREMENTS.</p>	<p>RL10011</p>
	PRE-CLOSEOUT TESTING AND INSPECTION	<p>THE FOLLOWING TESTS ARE PERFORMED AT THE CARD/COMPONENT LEVEL DURING MANUFACTURING:</p> <ul style="list-style-type: none"> <li>- PARTIAL IMPACT NOISE DETECTION (PIND) TEST ON HYBRID MICROCIRCUITS AND CAVITY TYPE DEVICES.</li> <li>- ULTRASONIC SCAN TEST FOR DELAMINATION CERAMIC ON CAPACITORS (CGRDS AND CDR06).</li> <li>- BURN-IN PERIOD FOR ELECTRICAL PARTS.</li> <li>- INSULATION RESISTANCE AND CONTINUITY TEST.</li> <li>- DIELECTRIC WITHSTANDING VOLTAGE.</li> <li>- FUNCTIONAL TEST.</li> <li>- X-RAY OF CONTROLLER INTERNAL CABLES AND WIRES.</li> </ul>	<p>DSHG8977A1</p> <p>DSHG8977A1 85M0392B DSHG8977A1 DSHG8977A1 DSHG8977A1 DSHG8977A1</p>

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CIL ITEM: F200-CB-01		INSPECTION AND TEST	
POSSIBLE CAUSES	SIGNIFICANT CHARACTERISTICS	INSPECTION(S)/TEST(S)	DOCUMENT REF.
		<p>THE FOLLOWING INSPECTIONS ARE PERFORMED ON THE COMPLETED ASSEMBLY PRIOR TO FINAL CLOSURE:</p> <ul style="list-style-type: none"> <li>- INTERNAL AND EXTERNAL CLEANLINESS.</li> <li>- CARD DIMENSIONS ARE VERIFIED AT CARD ASSEMBLY.</li> <li>- CHASSIS CAVITY INSPECTION FOR CONTAMINATION.</li> <li>- ELECTRICAL INTERFACE CONNECTORS.</li> <li>- CARD INSTALLATION AND CONFIGURATION AUDIT.</li> <li>- CONFORMAL COATING OF EEE PARTS.</li> </ul>	<p>RC1493 DSHG8977A1 RC1493 RC1493 DSHG8977A1 DSHC3928</p>
	ACCEPTANCE TESTS	<p>THE FOLLOWING TESTS ARE PERFORMED BY HONEYWELL DURING ACCEPTANCE TESTING:</p> <ul style="list-style-type: none"> <li>- HERMETIC SEAL AND PRESSURIZATION PORT LEAK TEST.</li> <li>- FUNCTIONAL TEST INCLUDING:                             <ul style="list-style-type: none"> <li>- OUTPUT INTERFACE,</li> <li>- CHECKOUT,</li> <li>- OPERATION,</li> <li>- CONTROLLER CHECKOUT.</li> </ul> </li> <li>- HIGH TEMPERATURE OPERATION.</li> <li>- LOW TEMPERATURE OPERATION.</li> <li>- VIBRATION TESTING.</li> <li>- FINAL FUNCTIONAL TEST.</li> </ul>	<p>RC1493 RC1493 RC1493 RC1493 RC1493 RC1493 RC1493 RC1493 RC1493</p>
	HOT FIRE ACCEPTANCE TESTING (GREEN RUN)	<p>CONTROLLER OPERATION IS VERIFIED THROUGH ENGINE HOT FIRE ACCEPTANCE TESTING.</p>	<p>RL00561</p>
	HERMETIC SEAL INTEGRITY	<p>INTERNAL PRESSURE IS MONITORED DURING CONTROLLER OPERATION.</p>	<p>OMRSD V41AND.040</p>
	FLIGHT FLOW TESTING	<p>THE FOLLOWING TESTS ARE PERFORMED DURING FLIGHT FLOW VEHICLE PROCESSING AND AFTER ANY MAINTENANCE OR REPLACEMENT:</p> <ul style="list-style-type: none"> <li>- CONTROLLER CHECKOUT.</li> <li>- SENSOR CHECKOUT.</li> <li>- FLIGHT READINESS TEST.</li> <li>- SELF-TEST.</li> </ul>	<p>OMRSD V41AND.035 OMRSD V41A00.010 OMRSD V41AS0.030-A CP406R0008</p>

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CCL ITEM: F200-CB-01		INSPECTION AND TEST	
POSSIBLE CAUSES	SIGNIFICANT CHARACTERISTICS	INSPECTION(S)/TEST(S)	DOCUMENT REF.
ALL CAUSES:	RE1493 - CONTROLLER PRE-FLIGHT CHECKOUT  CONTINUOUS SELF-TEST	<p>THE FOLLOWING TESTS ARE PERFORMED DURING LAUNCH COUNTDOWN:</p> <ul style="list-style-type: none"> <li>- PRE-CRYD LOADING CHECKOUT INCLUDING CONTROLLER AND SENSORS.</li> <li>- SELF-TEST.</li> </ul> <p>CONTROLLER OPERATION IS VERIFIED BY THE GROUND LAUNCH SEQUENCER PRIOR TO ENGINE START BY ACCEPTANCE OF COMMANDS PURGE SEQUENCE 4, POWER LEVEL, AND START ENABLE.</p> <p>BITTE TEST IS PERFORMED EVERY MAJOR CYCLE TO VERIFY HARDWARE INTEGRITY. (LAST TEST)</p>	<p>RE1493</p> <p>OWASO SOOFAO.213 CP406R0008</p> <p>LAUNCH COMMIT CRITERIA BGE-4-1126</p> <p>CP406R0008</p>
<p>FAILURE HISTORY: COMPREHENSIVE FAILURE HISTORY DATA IS MAINTAINED IN THE PROBLEM REPORTING DATABASE (PRANS/PRACA). REFERENCE: NASA LETTER 8A21/88/308 AND ROCKETDYNE LETTER B0RC09761.</p>			

OPERATIONAL USE: FAILURE MODE CAN BE DETECTED IN REALTIME BY THE FLIGHT CONTROL TEAM WHO WILL EVALUATE EFFECTS UPON VEHICLE PERFORMANCE AND ABORT CAPABILITY. BASED ON THIS EVALUATION THE APPROPRIATE ABORT MODE OR SYSTEM CONFIGURATION WILL BE SELECTED. FAILURE DETECTION CUES AND ASSOCIATED SOME PERFORMANCE DATA HAVE BEEN COORDINATED BETWEEN THE ENGINEERING AND FLIGHT OPERATIONS ORGANIZATIONS WITH THE RESPONSES DOCUMENTED IN MISSION FLIGHT RULES.