

| FMEA NO. <u>2.9.3</u> CRITICALITY <u>2/2</u> | | SHUTTLE CCTV CRITICAL ITEMS LIST | UNIT <u>PTU</u> DWG NO. <u>2294822-502,503,504</u> SHEET <u>1</u> OF <u>8</u> |
|--|---|---|---|
| FAILURE MODE AND CAUSE | FAILURE EFFECT ON END ITEM | RATIONALE FOR ACCEPTANCE | |
| Loss of pan and tilt (electrical function). PTU A2 Power Supply Command/Clock Receivers | Loss of travel in the pan and tilt direction. Worst Case: Loss of mission critical video. | <p>DESIGN FEATURES</p> <p>The heritage for the PTU mechanisms is the designs used successfully on the lunar Rover equipment on the Apollo 15, 16, and 17 missions.</p> <p>All support bearings in the azimuth and elevation axes are conservatively designed when compared to the launch load environment.</p> <p>The design was prepared by a detailed finite element analysis of the structure, taking into account the derating for the fatigue cycles represented by 100 missions. A series of developmental tests were conducted to verify the analytical models for the structure and drive train analyses. Reviews were held at preliminary design and critical design review levels to evaluate the designs and test data.</p> <p>The PTU has been used on 24 missions at four bulkhead locations and at the RMS elbow location without a failure in the drive train, axis support mechanisms, or structure.</p> <p>The mounting provision from the PTU base to the orbiter structure and RMS arm was analyzed for worst-case loading loads and showed adequate margins.</p> <p>BARE BOARD DESIGN (A2)</p> <p>The design of the associated A2 board is constructed from laminated copper-clad epoxy glass sheets (NEMA G-10) Grade FR-4), PER MIL-P-55617A. Circuit connections are made through printed traces which run from point to point on the board surfaces. Every trace terminates at an annular ring. The annular ring surrounds the hole in which a component lead or terminal is located. This ring provides a footing for the solder, ensuring good mechanical and electrical performance. Its size and shape are governed by MIL-P-55640 as are trace widths, spacing and routing. These requirements are reiterated specifically in drawing notes to further assure compliance. Variations between the artwork master and the final product (due to irregularities of the etching process) are also controlled by drawing notes. This prevents making defective boards from good artwork. Holes which house no lead or terminal, but serve only to electrically interconnect the different board layers, contain stitch bars for mechanical support and increased reliability.</p> <p>The thru holes are drilled from a drill tape thus eliminating the possibility of human error and allowing tight control over hole and annular ring concentricity, an important reliability criterion. After drilling and etching, All copper cladding is tin-lead plated per MIL-STD-1495. This provides for easy and reliable soldering at the time of board assembly, even after periods of prolonged storage.</p> | |

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| FAILURE MODE AND CAUSE | FAILURE EFFECT ON END ITEM | RATIONALE FOR ACCEPTANCE | |
| Loss of pan and tilt (electrical function). PIU A2 Power Supply Command/Clock Receivers | Loss of travel in the pan and tilt direction. Worst Case: Loss of mission critical video. | DESIGN FEATURES (Continued) BOARD ASSEMBLY DESIGN (A2) All components are installed in a manner which assures maximum reliability. Component leads are pre-tinned, allowing total wetting of solder joints. All leads are formed to provide stress relief and the bodies of large components are staked. Special mounting and handling instructions are included in each drawing required after final assembly. The board is coated with urethane which protects against humidity and contamination. BOARD PLACEMENT The A2 board is secured in the electronics assembly by gold-plated beryllium copper card guides. Connections are made to the mother board with blind-mated connectors. Disengagement during launch is prevented by a cover which spans the board's free edge. | |

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|---|---|--|
| FAILURE MODE AND CAUSE | FAILURE EFFECT ON END ITEM | RATIONALE FOR ACCEPTANCE |
| Loss of pan and tilt (electrical function). PIU 02 Power Supply Command/Clock Receivers | Loss of travel in the pan and tilt direction. Worst Case: Loss of mission critical video. | QUALIFICATION TESTS For Qualification Test Flow, see Table 2 located at the front of this book. |

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| FAILURE MODE AND CAUSE | FAILURE EFFECT ON END ITEM | RATIONALE FOR ACCEPTANCE | | | | | | | | | | | | | | | | |
|--|---|--|----------|--|------------|-------------------------|-------------|--------------------|----------------|-------------------|-------------|---------|---------|---|--------|---|---------|---|
| Loss of pan and tilt (electrical function). PTU A2 Power Supply Command/Clock Receivers | Loss of travel in the pan and tilt direction. Worst Case: Loss of mission critical video. | <p>ACCEPTANCE TEST</p> <p>The CCTV systems' PTU is subjected directly, without vibration isolators which might be used in their normal installation, to the following testing:</p> <ul style="list-style-type: none"> • Vibration: <table style="margin-left: 20px;"> <tr> <td>20-80Hz:</td> <td>3 dB/Oct-rise From 0.01 G²/Hz</td> </tr> <tr> <td>80-350 Hz:</td> <td>0.04 G²/Hz</td> </tr> <tr> <td>350-750 Hz:</td> <td>-3 dB/10 Oct-slope</td> </tr> <tr> <td>Test Duration:</td> <td>1 Minute per Axis</td> </tr> <tr> <td>Test Level:</td> <td>6.6 Gms</td> </tr> </table> • Thermal Vacuum: In a pressure of 1X10⁻⁵ Torr, the temperature shall be as follows: <table style="margin-left: 20px;"> <tr> <td>125° F:</td> <td>Time to stabilize equipment plus 1 hour</td> </tr> <tr> <td>25° F:</td> <td>Time to stabilize equipment plus 1 hour</td> </tr> <tr> <td>125° F:</td> <td>Time to stabilize equipment plus 1 hour</td> </tr> </table> <p>The PTU may not have been subjected to the vacuum condition.</p> <p>For Acceptance Test flow, see table 1 located at the front of this book.</p> <p>OPERATIONAL TESTS</p> <p>In order to verify that CCTV components are operational, a test must verify the health of all the command related components from the PHS (A7A1) panel switch, through the RCU, through the sync lines to the Camera/PTU, to the Camera/PTU command decoder. The test must also verify the camera's ability to produce video, the VSU's ability to route video, and the monitor's ability to display video. A similar test would be performed to verify the MDM command path.</p> <p>Pre-Launch on Orbiter Test/In-Flight Test</p> <ol style="list-style-type: none"> 1. Power CCTV System. 2. Via the PHS panel, select a monitor as destination and the camera under test as source. 3. Send "Camera Power On" command from PHS panel. 4. Select "External Sync" on monitor. 5. Observe video displayed on monitor. Note that if video on monitor is synchronized (i.e., stable raster) then this indicates that the camera is receiving composite sync from the RCU and that the camera is producing synchronized video. 6. Send Pan, Tilt, Focus, Zoom, ALC, AND Gamma commands and visually (either via the monitor or direct observation) verify operation. 7. Select downlink as destination and camera under test as source. 8. Observe video routed to downlink. 9. Send "Camera Power Off" command via PHS panel. 10. Repeat Steps 3 through 9 except issue commands via the MDM command path. | 20-80Hz: | 3 dB/Oct-rise From 0.01 G ² /Hz | 80-350 Hz: | 0.04 G ² /Hz | 350-750 Hz: | -3 dB/10 Oct-slope | Test Duration: | 1 Minute per Axis | Test Level: | 6.6 Gms | 125° F: | Time to stabilize equipment plus 1 hour | 25° F: | Time to stabilize equipment plus 1 hour | 125° F: | Time to stabilize equipment plus 1 hour |
| 20-80Hz: | 3 dB/Oct-rise From 0.01 G ² /Hz | | | | | | | | | | | | | | | | | |
| 80-350 Hz: | 0.04 G ² /Hz | | | | | | | | | | | | | | | | | |
| 350-750 Hz: | -3 dB/10 Oct-slope | | | | | | | | | | | | | | | | | |
| Test Duration: | 1 Minute per Axis | | | | | | | | | | | | | | | | | |
| Test Level: | 6.6 Gms | | | | | | | | | | | | | | | | | |
| 125° F: | Time to stabilize equipment plus 1 hour | | | | | | | | | | | | | | | | | |
| 25° F: | Time to stabilize equipment plus 1 hour | | | | | | | | | | | | | | | | | |
| 125° F: | Time to stabilize equipment plus 1 hour | | | | | | | | | | | | | | | | | |

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| FAILURE MODE AND CAUSE | FAILURE EFFECT ON END ITEM | RATIONALE FOR ACCEPTANCE | |
| Loss of pan and tilt (electrical) function. PTU A2 Power Supply Command/Clock Receivers | Loss of travel in the pan and tilt direction. Worst Case: Loss of mission critical video. | <p>QA/INSPECTION</p> <p>Procurement Control - The PTU EEE Parts and hardware items are procured from approved vendors and suppliers, which meet the requirements set forth in the CCTV contract and Quality Plan Work Statement (WS-2593176). Resident DCAS personnel review all procurement documents to establish the need for GSI on selected parts (PAI 517).</p> <p>Incoming Inspection and Storage - Incoming Quality Inspections are made on all received materials and parts. Results are recorded by lot and retained in file by drawing and control numbers for future reference and traceability. All EEE parts are subjected to incoming acceptance tests as called for in PAI 315 - Incoming Inspection Test Instructions. Incoming flight parts are further processed in accordance with RCA 1846684 - Preconditioning and Acceptance Requirements for Electronic Parts, with the exception that DPA and PIND testing is not performed. Mechanical items are inspected per PAI 316 - Incoming Inspection Instructions for mechanical items, PAI 305 - Incoming Quality Control Inspection Instruction, and PAI 612 - Procedure for Processing Incoming or Purchased Parts Designated for Flight Use. Accepted items are delivered to Material Controlled Stores and retained under specified conditions until fabrication is required. Non-conforming materials are held for Material Review Board (MRB) disposition. (PAI 307, PAI IQC-531).</p> <p>Board Assembly & Test - Prior to the start of PTU board assembly, all items are verified to be correct by stock room personnel, as the items are accumulated to form a kit. The items are verified again by the operator who assembles the kit by checking against the as-built-parts-list (ABPL). DCAS Mandatory Inspection Points are designated for all printed circuit, wire wrap and welded wire boards, plus harness connectors for soldering wiring, crimping, solder splices and quality workmanship prior to coating of the component side of boards and sleeving of harnesses.</p> <p>Specific PTU board assembly and test instructions are provided in drawing notes, and applicable documents are called out in the Fabrication Procedure and Record (FPR-2294822) and parts list PL 2294822. These include wire connection List 2295901, Process Standard RTV-566 228081, Process Standard - Bonding micro tape 2280889, Specification Soldering 2280749, Specification Name Plate Application 1960167, Specification - Crimping 2280860, Specification - Bonding and Staking 2280878, Specification - Urethane coating 2280877, Specification - Locking compound 2026116, Specification Epoxy Adhesive 2010985, Specification - Marking 2280876, Specification-Workmanship 8030035, Specification Bonding and Staking 2280875.</p> | |

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| FAILURE MODE AND CAUSE | FAILURE EFFECT ON END ITEM | RATIONALE FOR ACCEPTANCE |
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| <p>Loss of pan and tilt (electrical function).</p> <p>PTU 52 Power Supply Command/Clock Receivers</p> | <p>Loss of travel in the pan and tilt direction.</p> <p>Worst Case: Loss of mission critical video.</p> | <p><u>QA/INSPECTION</u></p> <p><u>PTU Assembly and Test</u> - An open box test is performed per TP-IT-2294822, and an Acceptance Test per TP-AT-2294822, including vibration and thermal vacuum. Torques are specified and witnessed, traceability numbers are recorded and calibrated tools are checked prior to use. RCA Quality and DCAS inspections are performed at the completion of specified FPR operations in accordance with PAI-204, PAI-205, PAI 206 and PAI 217. DCAS personnel witness PTU button-up and critical torquing. R and DCAS personnel monitor acceptance tests and review the test data/results. These personnel also inspect for conformance after all repair, rework and retest.</p> <p><u>Preparation for Shipment</u> - The PTU is packaged according to CCTV Letter 8011 and 2280746, Process standard for Packaging and Handling guidelines. All related documentation including assembly drawings, Parts List, ADPL, Test Data, etc. is gathered and held in a documentation folder assigned specifically to each assembly. This folder is retained for reference. An EIDP is prepared for each PTU in accordance with the requirements of WS-2593176. RCA QC and DCAS personnel witness crating, packaging, packing and marking, and review the EIDP for completeness and accuracy.</p> |

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| FAILURE MODE AND CAUSE | FAILURE EFFECT ON END ITEM | RATIONALE FOR ACCEPTANCE |
| Loss of pan and tilt (electrical function). PTU A2 Power Supply Command/Clock Receivers | Loss of travel in the pan and tilt direction. Worst Case: Loss of mission critical video. | FAILURE HISTORY TDR A2548 Log #0905 S/W021-502 Description: Flight Failure. Spacecraft Level. PTU did not tilt and was slow to pan during FLT of STS-11 (Ref) PV2-060939). Cause: Protruding Q1 mounting hardware on A2 board produced short to ground through C2 on adjacent A1 Board. Q1 transistor on A2 Board had open base to emitter junction. Corrective Action: ECN CCT 1146 was issued to change length of Q1 mounting hardware. Q1 was replaced in addition to C2 & C4 on A1 board. PTUs in field were inspected and reworked to comply with ECN CCT 1146. |

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| FAILURE MODE AND CAUSE | FAILURE EFFECT ON END ITEM | RATIONALE FOR ACCEPTANCE |
|---|--|--|
| <p>loss of pan and tilt (electrical function).</p> <p><u>PTU</u> <u>A2</u> Power Supply Command/Clock Receivers</p> | <p>Loss of travel in the pan and tilt direction.</p> <p><u>Worst Case:</u> Loss of mission critical video.</p> | <p><u>DESIGN FEATURES</u></p> <p>Possible loss of major mission objectives due to inability to position camera for desired FOV.</p> <p><u>CREW ACTION</u></p> <p>If possible, continue mission using alternate visual cues.</p> <p><u>CREW TRAINING</u></p> <p>Crew should be trained to use possible alternates to CCTV.</p> <p><u>MISSION CONSTRAINT</u></p> <p>Where possible procedures should be designed so they can be accomplished without CCTV.</p> |