

CRITICAL ITEMS LIST

REFERENCE DES. FOR DS - 1
 NAME/QUANTITY: Pip pin, Double Socket
 DRAWING REFERENCE: 58789R28-21NL8C4

PROJECT: DTD #71
 LRU NAME/QUANTITY: Double PFR Socket/1
 UELPARTNUMBER: SED39122848

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 SUBSYSTEM EVA
 EFFECTIVITY: ALL ORBITERS

FAILURE MODE NUMBER	CRITICALITY	FAILURE EFFECT	RETENTION RATIONALE
DS-1-1	1R/2		
FUNCTION Secures articulating socket and/or PFR to double PFR socket during launch/landing and on-orbit use (articulating socket and PFR not in series for launch/landing).		END ITEM On hitch pin failure the articulating socket or PFR could become free in the payload bay.	I. Design Feature to Minimize the Chance of the Failure Mode A. Design The double PFR socket was designed to an ultimate structural safety factor of 1.4 by test and 1.4 by analysis. B. Tolerances Sufficient tolerances were used in the double socket design to prevent pip pin release by expansion and contraction of material due to temperature extremes or on-orbit use, as documented during each mission's thermal analysis. C. Materials - Major Components 1) Pip pin 58789R28-21NL8C4; Stainless Steel Shank and Balls with hitch pin. or 2) Pip pin MS17990C821; Stainless Steel Shank and Balls. II. Testing and Analysis A. Acceptance Testing 1. PIA A full pre-installation acceptance (PIA) test shall be performed on the double PFR socket per the requirements found in HRD #JSC-38153 before it is delivered to KSC to support any STS flight. The PIA will verify that the socket is functioning within tolerances and that the assembly is clean. 2. Vibration The PFR socket assembly shall be exposed to acceptance level vibration loads per HRD #JSC-38153. The test shall verify that the socket assembly is free of manufacturing defects and tolerance problems.
FAILURE MODE AND CAUSE MODE A double socket pip pin releases during launch or landing and allows the articulating socket or PFR to become free.		MISSION None	
CAUSE(S) 1) Piece part(s) failure of pip pin 2) Vibration		CREW / VEHICLE Possible damage to orbiter	
REUNDANCY SCREENS A - Pass B - Pass C - Pass	REMAINING PATHS 1) Hitch pin on pip pin	INTERFACES Articulating sockets: SED33105422-301 (LESC) -303,305 SED39124685-302 (OSS) PFRs: SED33105308-301 SED33105708-301,303	
MISSION PHASE	CORRECTIVE ACTION TIMES		
	TIME TO EFFECT	TIME TO CORRECT	
Launch/Landing	Seconds	None	

PREPARED BY M. M. GIETZEL

REVISION BASIC

SUPERSEDING DATE: NONE

DATE: 7/26/04

CRITICAL ITEMS LIST

REFERENCE DESIGNATOR: DS-1
 NAME / QUANTITY: Pip pins, Double Socket
 DRAWING REFERENCE: 5678926-21NLSA

PROJECT: DTD 871
 LRU NAME / QUANTITY: Double PFR Socket
 LRU PART NUMBER: SED39122688

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 SUBSYSTEM: EVA
 EFFECTIVITY: ALL ORBITERS

FAILURE MODE NUMBER DS-1-1	CRITICALITY 1R/2	FAILURE EFFECT	RETENTION RATIONALE																								
FUNCTION Secures articulating socket and/or PFR to double PFR socket during launch/landing and on-orbit use (articulating socket and PFR not in series for launch/landing).		END ITEM On hitch pin failure the articulating socket or PFR could become free in the payload bay.	A. Acceptance Testing (continued) 3. Pip Pin Program All Pip Pins used in the double socket shall be exposed to a separate dedicated acceptance test program to assure the project that the pip pins are at acceptable conditions which will help prevent a failure on-orbit. Reference: Hardware Requirements Document (HRD) #JSC-38159. a. Cleaning All pip pins shall be cleaned with Freon to remove any lubricant from the pin which could be the cause of binding in the internal pin mechanism due to cold temperature. b. Thermal All of the flight and flight back-up pip pins shall be exposed to a cold chamber test to a range of -100°F to -130°F and must operate properly at that temperature. c. Vibration All flight and back-up pip pins shall be exposed to an AVT/QAVT vibration level derived from SP-T-0023B as described in JSC-38153. The duration shall be three minutes per axis. The vibration levels are: <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th>Frequency (Hz)</th> <th>Slope (dB/oct.)</th> <th>Constant Level G²/Hz</th> <th>Overall Gms</th> </tr> </thead> <tbody> <tr> <td>20</td> <td></td> <td>0.01</td> <td>6.08</td> </tr> <tr> <td>80</td> <td></td> <td>0.04</td> <td></td> </tr> <tr> <td>200</td> <td></td> <td>0.04</td> <td></td> </tr> <tr> <td>350</td> <td></td> <td>0.04</td> <td></td> </tr> <tr> <td>2000</td> <td></td> <td>0.007</td> <td></td> </tr> </tbody> </table>	Frequency (Hz)	Slope (dB/oct.)	Constant Level G ² /Hz	Overall Gms	20		0.01	6.08	80		0.04		200		0.04		350		0.04		2000		0.007	
Frequency (Hz)	Slope (dB/oct.)	Constant Level G ² /Hz		Overall Gms																							
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FAILURE MODE AND CAUSE MODE A double socket pip pin releases during launch or landing and allows the articulating socket or PFR to become free.		MISSION None																									
CAUSE(S) 1) Piece part(s) failure of pip pin 2) Vibration		CREW / VEHICLE Possible damage to orbiter																									
REDUNDANCY SCREENS A - Pass B - Pass C - Pass	REMAINING PATHS 1) Hitch pin on pip pin	INTERFACES Articulating sockets: SED33105422-301 (LSC) :303,305 SED39124666-302 (OSS) PFRs: SED33105308-301 SED33105708-301, -303																									
MISSION PHASE	CORRECTIVE ACTION TIMES																										
	TIME TO EFFECT	TIME TO CORRECT																									
Launch/Landing	Seconds	None																									

PREPARED BY: M. M. BIETZEL

REVISION: BASIC

SUPERSEDING DATE: NONE

DATE: 7/28/94

CRITICAL ITEMS LIST

REFERENCE DESIGNATOR: DS - 1

NAME/QUANTITY: Pip pin, Double Socket

DRAWING REFERENCE: 58788R24-21N18C4

PROJECT: OTO 871

LRU NAME/QUANTITY: Double PFR Socket/1

LRU PART NUMBER: SED3122848

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SUBSYSTEM: EVA

EFFECTIVITY: ALL ORBITERS

FAILURE MODE NUMBER DS-1-1	CRITICALITY 1R/2	FAILURE EFFECT	RETENTION RATIONALE
FUNCTION Secures articulating socket and/or PFR to double PFR socket during launch/landing and on-orbit use (articulating socket and PFR not in series for launch/landing).		END ITEM On hitch pin failure the articulating socket or PFR could become free in the payload bay.	<p>A. Acceptance Testing (continued)</p> <p>d. Functional</p> <p>All pip pins shall be functionally operated and operation loads recorded prior to and immediately after all acceptance level testing to determine if there has been any adverse effects due to the test environment.</p> <p>B. Certification Testing</p> <p>1. The double socket was tested to the EVA "general use" loads per HRD #JSC-38153 with a factor of safety of 1.4 (TPS #DW93200820).</p> <p>2. Thermal Vacuum</p> <p>The double socket was exposed to a cold temperature (-120°F) vacuum (1x10⁻⁵ torrs) environment per TPS # 588320218 (HST MTV #3). This test was used to check the functionality of the pip pin with successful results Ref. JSC-38153.</p> <p>3. Functionals</p> <p>The double socket was functionally operated prior to and immediately after all acceptance and certification tests to verify that the test environment did not degrade the hardware performance.</p>
FAILURE MODE AND CAUSE MODE A double socket pip pin releases during launch or landing and allows the articulating socket or PFR to become free.		MISSION None	
CAUSE(S) 1) Piece part(s) failure of pip pin 2) Vibration		CREW / VEHICLE Possible damage to orbiter	
REDUNDANCY SCREENS A - Pass B - Pass C - Pass	REMAINING PATHS 1) Hitch pin on pip pin	INTERFACES Articulating sockets: SED33105422-301 (LESC) -303,-306 SED09124888-302 (OSS) PFRs: SED33105308-301 SED33105706-301,-303	
MISSION PHASE	CORRECTIVE ACTION TIMES TIME TO EFFECT TIME TO CORRECT		
Launch/Landing	Seconds	None	

PREPARED BY: M. M. GIETZEL

REVISION: BASIC

SUPERSEDING DATE: NONE

DATE: 7/20/84

CRITICAL ITEMS LIST

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REFERENCE DESIGNATOR: DS - 1
 NAME / QUANTITY: Pip pin, Double Socket
 DRAWING REFERENCE: 56789P26-21N1E04

PROJECT: DTO #71
 LRU NAME / QUANTITY: Double PFR Socket/1
 SUBPART NUMBER: SED39122848

SUBSYSTEM: EVA
 EFFECTIVITY: ALL ORBITERS

FAILURE MODE NUMBER DS-1-1	CRITICALITY 1R/2	FAILURE EFFECT	RETENTION RATIONALE									
FUNCTION Secures articulating socket and/or PFR to double PFR socket during launch/landing and on-orbit use (articulating socket and PFR not in series for launch/landing).		END ITEM On hitch pin failure the articulating socket or PFR could become free in the payload bay.	B. Certification Analysis (continued) 4. Qual Vibration Testing The double socket was part of a successful QAVT/QVT PFR test, TPS FV9420042. The following levels are per HRD #JSC-38153. <table style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: left;">Frequency (Hz)</th> <th style="text-align: left;">G Level</th> <th style="text-align: left;">Duration per axis x,y,z</th> </tr> </thead> <tbody> <tr> <td>20-2000</td> <td>7.84</td> <td>10 min</td> </tr> <tr> <td>20-2000</td> <td>7.80</td> <td>33 min 20 sec</td> </tr> </tbody> </table> 5. All double socket components were analyzed to the following induced environments to verify that the assembly can withstand the environment levels: 1. Requirements Source a. Structures - LH JSC-38153 - Fracture JSC-38153 The double socket was designed to the following thermal environments: b. Temperature - Hot (+250°F) JSC-38153 - Cold (-120°F) JSC-38153 III. Inspection A. Manufacturing 1. The double socket is verified for conformance to the applicable drawings. 2. The pip pin is not fracture critical per drawings. 3. The pip pins are verified upon delivery.	Frequency (Hz)	G Level	Duration per axis x,y,z	20-2000	7.84	10 min	20-2000	7.80	33 min 20 sec
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Seconds	None											

PREPARED BY: M. M. GIETZEL

REVISION: BASIC

SUPERSEDING DATE: NONE

DATE: 7/20/84

CRITIC ITEMS LIST

REFERENCE DESIGNATOR: DS - 1
 NAME / QUANTITY: Pip pin, Double Socket
 DRAWING REFERENCE: 58799R28-21NL&C4

PROJECT: DTO 671
 LRU NAME / QUANTITY: Double PFR Socket/1
 LRU PART NUMBER: SED39122648

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 SUBSYSTEM: EVA
 EFFECTIVITY: ALL ORBITERS

FAILURE MODE NUMBER	CRITICALITY	FAILURE EFFECT	RETENTION RATIONALE
DS-1-1	1R/2		
FUNCTION Secures articulating socket and/or PFR to double PFR socket during launch/landing and on-orbit use (articulating socket and PFR not in series for launch/landing).		END ITEM On hitch pin failure the articulating socket or PFR could become free in the payload bay.	B. Assembly 1. The double socket shall be cleaned and verified to level VC per JSC 5322B. Once cleaned, the double socket shall be bagged to prevent any contamination from entering the unit. C. Testing 1. The pip pins are fully inspected for any signs of loose parts as part of the pre/post functional tests performed prior to and immediately after all certification and acceptance tests. 2. After each flight, the assembly will be fully inspected. Prior to each flight, a PIA will be performed to verify that the hardware, especially the pip pin, is operating properly. IV. Failure History A. None V. Operations A. <u>Effects of Failure</u> The PFR or articulating socket could become free in the payload bay during launch/landing. B. <u>Crew Actions</u> None. C. <u>Training</u> None. D. <u>Mission Constraints</u> None E. <u>In Flight Check-Outs</u> None
FAILURE MODE AND CAUSE MODE A double socket pip pin releases during launch or landing and allows the articulating socket or PFR to become free.		MISSION None	
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MISSION PHASE	CORRECTIVE ACTION TIMES		
	TIME TO EFFECT	TIME TO CORRECT	
Launch/Landing	Seconds	None	

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REVISION: BASIC

SUPERSEDING DATE: NONE

DATE: 7/20/84